

INPUT TERMINAL PIN ASSIGNMENT

Input Signal & Power (LVDS, Connector : JAE FI-SEB20P-HF-E1500
Mating Connector : JAE FI-SE20M-HF)

PIN NO	SYMBOL	FUNCTION	POLARITY	REMARK
1	VDD	POWER SUPPLY +3.3V		
2	VDD	POWER SUPPLY +3.3V		
3	GND	GROUND		
4	GND	GROUND		
5	RxIN0-	LVDS Differential Data INPUT	Negative	R0~R5 G0
6	RxIN0+	LVDS Differential Data INPUT	Positive	
7	GND	GROUND		
8	RxIN1-	LVDS Differential Data INPUT	Negative	G1~G5 B0~B1
9	RxIN1+	LVDS Differential Data INPUT	Positive	
10	GND	GROUND		
11	RxIN2-	LVDS Differential Data INPUT	Negative	B2~B5,DE Hsync,Vsync
12	RxIN2+	LVDS Differential Data INPUT	Positive	
13	GND	GROUND		
14	RxCLKIN-	LVDS Differential Data INPUT	Negative	
15	RxCLKIN+	LVDS Differential Data INPUT	Positive	
16	GND	GROUND		
17	NC	No Connection		
18	NC	No Connection		
19	GND	GROUND		
20	GND	GROUND		